

**Mississippi Valley State University
Department of Engineering Technology
College of Professional Studies**

Course Syllabus

ET 142L Semiconductors and Devices Lab,

Lab: Tuesday: 3:00 – 4:50 am
Science and Technology Bldg, Classroom: 1112
Credit Hours: 1

Mr. Lemorris Strong – Assistant Professor
Office #: (662) 254-3409 or cell # (662) 207-3709
Email Address lemorris.strong@mvsu.edu
Office Hours: As posted

Pre-requisite: A grade of “C” or better in ET 141L, Basic Electronics Lab.

LAB DESCRIPTION: The Lab activities track the course lecture to provide a mean for students to gain experience (hands-on) in Semiconductor and Devices through projects and equipment demonstration. Students will work individually and collectively to complete projects and assignments.

STUDENT LEARNING OUTCOMES: Upon the successful completion of this Lab, the students will be able to, either orally, or in writing, and with minimum use of outside references, complete the following objectives.

1. Understand Magnetism and Electromagnetism concepts
2. Explain Inductive Reactance and Capacitive Reactance in AC
3. Identify and explain semiconductor and devices characteristics, (Diodes, Bipolar-Junction Transistors (BJT), BJT Amplifier Circuits, Field-Effect Transistors, Operational Amplifier (OP-Amp), Oscillators Multi-vibrators, and Thyristors)
4. Design, construct, and troubleshoot basic circuits
5. Test components function
6. Understand the function and the technique on power supplies

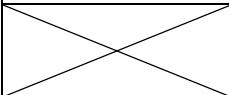
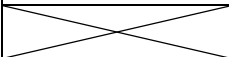
TEXT: *Foundations of Electronics, 5-th edition, by Russell L. Meade. ISBN: 13 9781418041830*

LAB METHOD: Two (2) hours of laboratory weekly.

EXPECTATIONS: This course of study has been prepared to inform you about the lab requirements, contents, teaching methods, evaluation process and requirements for students. You are expected to read the information carefully so that you understand what is expected of you and the lab requirements that must be met to successfully complete the course. You will be held responsible and accountable for knowing the schedule of class meeting and examinations. It is recommended that you read and study all assigned materials. If you do not understand the material, you should promptly consult your instructor.

GRADE STANDARDS AND WEIGHTS FOR LAB: All projects must be submitted for grading by the due date. Assignments that are not typed must be neatly written on lineless paper. The lab project is worth 50 points per assignments. Mid-term and Final score are 100 points

ASSESSMENT SHEET ET

Chapter/Project	MEANS OF ASSESSMENT	POINTS	GRADE POINTS
Chapter 9	Magnetism and Electromagnetism	50	
Project 40 & 41	Inductance	100	
Projects 45 & 46	Basic Transformer Characteristics	100	
Projects 47 & 48	Capacitance	100	
Projects 61 & 62	Semiconductor Materials	100	
Projects 63 & 64	Diode and Diode Circuit	100	
	Med-Term	150	
	Mid-Term Grade Projects points added + mid term ÷ by 6 = grade point	100	*
Projects 65, 66, and 67	Power Supply Circuit	150	
Project 68 & 69	Bipolar Junction Transistors	100	
Projects 74 and 75	Field-Effect Transistors and Circuits	100	
Projects 76, 77, and 78	Operational Amplifiers	150	
Projects 81 and 82	Thyristor	100	
Part 28	Optoelectronics	100	
	Final	100	
	Incentive credit	8	
Final Grade	Total projects points added + med term + final score ÷ by 7 = final grade point	1500	

*See grading standard

Grading system:

A = 91 – 100	or	1410 - 1500
B = 81 - 90	or	1310 - 1400
C = 71 - 80	or	1210 - 1300
D = 61 - 70	or	1110 - 1200
F = 61 - Below		

INCENTIVE: On time twenty (20) class days consecutive and receive two (2) points toward your mid-term and final. All typed assignments will receive two (2) points towards the assignment. Assignment turned in on time will receive two (2) points toward the assignment.

LAB PROJECTS: Lab projects are assigned as define in text book or by the instructor.

FEES: A fee may be required base upon the lab assignment

REFERENCES

1. *The Mechanical Universe and Beyond*, videotape: on reserve at the Library
2. *Circuit Tutor*, software: available on AT computers
3. The CD contained in the back of your class book
4. You Tube, (youtube.com) Electronics Programs

CLASS POLICES: The following Lab policies are in effect throughout this course to ensure that a positive and equitable learning environment exists for all lab members. Failure to follow these class policies may result in sanctions per the student handbook. Policies for lab include:

Attendance: **Students are expected to attend Lab.** Any absentees will have a negative effect upon your final lab grade. Students are responsible for and accountable for all information and all modifications to lab assignments. If a student misses a lab, he or she is responsible for the material missed – even if a valid excuse is presented.

Punctuality: Every student is expected to practice professional time management skills and report to lab on time.

Professional Language/Respect: Professional language, conduct, and respect for peers, the instructor, and the learning process are a basic requirement. This is a “G” rated course

Disruptive Behavior: Effective learning environments require the attention and positive contribution of both student and teacher. Sidebar or loud conversations, jokes, laughter, pranks, etc., between and among students in the classroom while class is in session are disruptive to the learning environment. Students disrupting the class/learning environment in such a manner are subject to grading penalties.

Others Attending Lab: Those who are not registered for the course are not permitted to attend. This includes, but is not limited to, boyfriends/girlfriends, children, siblings, spouses, etc.

Electronic Devices: Cell phones, iPads, iPods, etc. will not be used in class and should be turned OFF **BEFORE** you enter the classroom. Electronic device use will result in **immediate expulsion** from the lab.

Computer: Doing class computer usage has to be approval by the Instructor

Make-up Work: **No make-ups assignments are available. Assignment due dates are announced well in advance and are FIRM. There are no exceptions for athletic travel or other pre-planned absences. Students are encouraged to complete assignments EARLY and to submit them PRIOR TO THE DEADLINE.** Assignments will only be accepted as called for by the instructor, on or prior to the due date. **No make-ups will be given for missed assignments or examinations.** **An excuse from the Office of Student Affairs DOES NOT EXCUSE the student from work or assignments missed during the absence!**

Honor Code: Any work submitted is expected to be yours. Dishonesty (cheating) in any form will not be tolerated. Cheating will result in the student(s) involved being immediately dismissed from the class and receiving a grade of "F" for the course. Cheating includes submitting someone else's

work as your own or allowing someone else to submit your work as theirs. Cheating will be determined at the sole discretion of the instructor.

Other: All other student conduct policies are in full effect as explained in the Mississippi Valley State University Undergraduate Catalog.

DRESS POLICY; *There is no MVSU dress policy, however in this lab there will be NO pants sagging, NO hats, hoods, or any other headgear worn (females are allowed to wear hats). If this dress policy is violated you will be asked to leave the class room with a loss of 20 points. Only an excuse from the Department Head will allow you to return.*

Accommodating Special Needs Students: Students with learning or physical disabilities admitted to this class will be expected to perform the same level of work at the same proficiency as students without disabilities. However, where necessary, alternative methods will be used to accommodate any learning or physical condition if it is made known to the instructor in advance. No student will be turned away from this class because of a disabling condition.

Mississippi Valley State University is committed to providing reasonable accommodations for students with a documented disability. If you feel you are eligible to receive accommodations for a covered disability (medical, physical, psychiatric, learning, vision, hearing, etc.) and would like to request it for this course, you must be registered with the Services for Students with Disabilities (SSD) program administered by University College. It is recommended that you visit the Disabilities Office located inside the EMAP Computer Lab in the Technical Education (IT) Building to register for the program at the beginning of each semester. If you are determined to be eligible after your confidential consultation, you will be provided with a Memo of Accommodations that must be submitted to each of your instructors.

For more information or to schedule an appointment, please contact Mr. Billy Benson, Jr. via phone or email at 662-254-3005 or billy.benson@mvsu.edu.

Withdrawals and Incompletes: A syllabus constitutes a contract between the student and the course instructor. Participation in this course indicates your acceptance of its teaching focus, requirements, and policies. Please review the syllabus and the course requirements as soon as possible. If you believe that the nature of this course does not meet your interests, needs or expectations, if you are not prepared for the amount of work involved or if you anticipate that the class meetings, assignment deadlines or abiding by the course policies will constitute an unacceptable hardship for you, you should drop the class by the drop/add deadline. **Incompletes will not be offered in this course.**